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| **CENTRAL RESEARCH LABORATORY BHARAT ELECTRONICS LIMITED,**  |
| **LIST OF CANDIDATES PROVISIONALLY SELECTED FOR THE POST OF PROJECT ENGINEER-I FOR CRL-GHAZIABAD AGAINST** **Advt No. 4926/PE & TE/02/HR/CRL-GAD/2024-25**  |
| **SL No.** |  **ROLL NO.**  | **NAME** | **DOB** | **POST NAME** |
| 1 | 20250308 | VIKRAM SINGH YADAV | 11.12.1997 | Project Engineer-I |
| 2 | 20250318 | FAIZAN AHMAD | 09.10.1997 | Project Engineer-I |
| 3 | 20250314 | VARUN | 20.09.1999 | Project Engineer-I |
| 4 | 20250316 | AZARUDDIN ANSARI | 10.07.1998 | Project Engineer-I |
| 5 | 20250309 | ANOOP SINGH | 10.02.1994 | Project Engineer-I |
| 6 | 20250306 | MANOJ KUMAR | 01.07.1990 | Project Engineer-I |

**Sd/-**

**DY. GENERAL MANAGER- HR&A (CRL-GAD)**